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May 18, 2005
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Application of: Khater et al.

Date: May 18, 2005

Serial No.: 10/604,988

Group Art Unit: 2818

Date Filed: August 29, 2003

Confirmation No.: 1987

Attorney Docket No.: FIS920030097US1

Examiner: Dang, Phuc T.

TITLE: BIPOLAR TRANSISTOR HAVING RAISED
EXTRINSIC BASE WITH SELECTABLE SELF-
ALIGNMENT AND METHODS OF FORMING
SAME

IBM Corporation
Intellectual Property Law
Bldg. 321/ FL 2 / Aisle J-1 M/D 482
2070 Route 52
Hopewell Junction, New York 12533-6531

STATEMENT UNDER 37 CFR 1.97(e)

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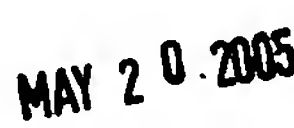
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Respectfully submitted,
Marwan H. Khater et al.

Date: May 18, 2005

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Approved for use through 07/31/2006. OMB 0651-0031

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Complete if Known

Application Number	10/604,988
Filing Date	August 29, 2003
First Named Inventor	Khater et al.
Art Unit	2818
Examiner Name	Dang, Phuc T.
Attorney Docket Number	FIS920030097US1

[illegible]

FOREIGN PATENT DOCUMENTS

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Examiner Signature		Date Considered	
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